

**In the Claims:**

Please amend claims 1 and 6, and add new claims 11-13 as indicated below. This listing of claims replaces all prior versions.

1. (Currently amended) A method of performing configuration or control of a subsystem, comprising: providing together with the subsystem a configuration/control unit having a controller portion and a storage portion storing configuration parameters; the configuration/control unit receiving an activation signal; and the configuration/control unit, in response to the activation signal, performing configuration or control of the subsystem, including storing at least one of the configuration parameters at a location within in a register of the subsystem.
2. (Previously presented) The method of claim 1 wherein the subsystem is a hardware subsystem, and the configuration/control unit is a hardware configuration/control unit.
3. (Previously presented) The method of claim 1 wherein the hardware subsystem and the hardware configuration/control unit are provided together within the same integrated circuit.
4. (Original) The method of claim 1 wherein the activation signal is a configuration/control ID.
5. (Previously presented) The method of claim 4 wherein the configuration/control unit is responsive to multiple different configuration/control IDs for performing different corresponding configuration or control actions with respect to the subsystem.
- 6 (Currently amended) A subsystem having self-configuration capabilities, comprising: a register section including multiple registers, the subsystem functioning differently depending on contents of the registers; and a configuration/control unit having a controller portion and a storage portion storing configuration parameters; wherein the

configuration/control unit is responsive to an activation signal for performing configuration or control of the subsystem, including storing at least one of the configuration parameters ~~at a location within~~ in one of the multiple resistors of the subsystem.

7. (Previously presented) The apparatus of claim 6 wherein subsystem is a hardware subsystem, and the configuration/control unit is a hardware configuration/control unit.

8 (Previously presented) The apparatus of claim 7 wherein the hardware subsystem and the hardware configuration/control unit are provided together within the same integrated circuit.

9. (Original) The apparatus of claim 6 wherein the activation signal is a configuration/control ID.

10 (Previously presented) The apparatus of claim 9 wherein the configuration/control unit is responsive to multiple different configuration/control IDs for performing different corresponding configuration or control actions with respect to the subsystem.

11. (New) For use in a system that includes a processor coupled to a hardware subsystem via a system bus, the hardware subsystem including a configuration/control unit and a plurality of registers, a method of configuring the subsystem comprising:

storing a plurality of configuration parameters in the configuration/control unit;  
and

responsive to the configuration/control unit receiving a single configuration/control ID from the processor, writing one or more of the plurality of configuration parameters from the configuration/control unit to one or more of the plurality of registers.

12. (New) The method of claim 11, wherein the configuration/control unit is a state machine.
13. (New) The method of claim 11, wherein the subsystem is a USB block comprising a plurality of ports that can operate in different modes responsive to which of the plurality of configuration parameters are written to which of the plurality of registers.